

<b>Notice of References Cited</b>	Application/Control No. 10/726,529	Applicant(s)/Patent Under Reexamination ZHANG, HONGYONG	
	Examiner Quoc D Hoang	Art Unit 2818	Page 1 of 1

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	D	US-			
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*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S.Wolf and R.N Tauber, Silicon Processing for the VLSI Era, Lattice Press, Volume 1:Process Technology, pages 177-178
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.